



POL and DET are either side of the detect switch

3.3Vsd
is for sleep mode power savings
connect this to system 3.3V via some kind of power switch
if you don't care about sleep mode, connect this directly to system 3.3V

3.3V
CD pulls up to 3.3V so cpu can sense card insert even in sleep mode
connect this to system 3.3V, or if you don't care about that,
connect 3.3V pin to 3.3Vsd pin externally

For sleep mode handling, connect smarc SDIO_PWR_EN
signal to the enable input of a power switch.
Dito, and to enable booting from SD card, use a 10kOhm
pullup on SDIO_PWR_EN to system 3.3V.

U1 and U2 NC pins might be connected or not
depending what is convenient at the pcb layout

R0 is an array of 8 parallel resistors in this version, others uses
metric 1005 chip resistors, SGeT design guide uses 0 Ohm
Embedian uses 100 Ohm for WP and CD, and 22 Ohm the other signals

Weak pullup (50k to 3.3Vsd) are optionally required
depending on used smarc module

R3 and R4 are optional, they should be pulled up on the
smarc module (though they are used in SGeT design guide)



		<h2>SMARC carrier board SD card if. protection</h2>	